Department of Electronics & Communication Engineering

Faculty of Engineering, Integral University, Lucknow

Course Distribution

Semiconductor Device Modeling & Circuit Simulation (GEC-101)

M.Tech (Part Time) Electronics Circuit & System Second Semester

Faculty Adviser : Shrish Bajpai

Course Web Address : http://sbajpai.yolasite.com/semiconductor-device-modeling.php

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	Logic & Timing Simulation	Lecture Notes	

Text Books :

- 1. Ben G. Streetman & Sanjay Banerjee, "Solid State Electronic Devices", Sixth Edition, Prentice Hall of India Private Limited, India.
- 2. Jacob Millman & Christos C. Halkias,, "Integrated Electronics", Second Edition, Tata McGraw Hill Publication, India.
- 3. Kannan Kanno, "Semiconductor Devices and Physics", Prentice Hall of India Private Limited, India.
- 4. Jan M. Rabaey, Anantha Chandrakasan and Borivoje Nikolic, "Digital Integrated Circuits", Second Edition, Prentice Hall of India Private Limited, India.
- 5. M.Rashid, "Introduction to PSpice",